

Form PTO-1449

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)
3526.2US (97-1136.2)

Application Number
09/585,682

Applicant **Ko et al.**Filing Date **June 1, 2000**Group Art Unit **2815**

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
C.C.	5,828,096	10/1998	Ohno et al.	257	306	—
C.C.	5,831,899	11/1998	Wang et al.	365	156	—

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

C.C.		Wolf, Stanley, "Silicon Processing for the VLSI Era," cover pages and pages 194-195, Volume 2: <u>Process Integration</u> .

EXAMINER



DATE CONSIDERED

2/8/02

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.